uhd\_usrp\_probe

**System Variable Configuration for Send and Receiver buffer**

Because I get the send buffer could not be resized sufficiently error I have to run

The next command to execute sysctl.conf

It’s suppose to auto run after I boot up but something else must run to size the

Send buffer back smaller because I have to keep running it

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[INFO] [X300] X300 initialization sequence...

[INFO] [X300] Maximum frame size: 1472 bytes.

[INFO] [X300] Radio 1x clock: 200 MHz

[INFO] [GPS] Found an internal GPSDO: LC\_XO, Firmware Rev 0.929a

[WARNING] [UDP] The send buffer could not be resized sufficiently.

Target sock buff size: 24266666 bytes.

Actual sock buff size: 1048576 bytes.

See the transport application notes on buffer resizing.

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Sudo sysctl -p

uhd\_usrp\_probe

klieberschnitzel@ubuntu18-04-3:~$ uhd\_usrp\_probe

[INFO] [UHD] linux; GNU C++ version 7.4.0; Boost\_106501; UHD\_3.14.1.HEAD-0-g0347a6d8

[INFO] [X300] X300 initialization sequence...

[INFO] [X300] Maximum frame size: 1472 bytes.

[INFO] [X300] Radio 1x clock: 200 MHz

[INFO] [GPS] Found an internal GPSDO: LC\_XO, Firmware Rev 0.929a

[INFO] [0/DmaFIFO\_0] Initializing block control (NOC ID: 0xF1F0D00000000000)

[INFO] [0/DmaFIFO\_0] BIST passed (Throughput: 1319 MB/s)

[INFO] [0/DmaFIFO\_0] BIST passed (Throughput: 1311 MB/s)

[INFO] [0/Radio\_0] Initializing block control (NOC ID: 0x12AD100000000001)

[INFO] [0/Radio\_1] Initializing block control (NOC ID: 0x12AD100000000001)

[INFO] [0/DDC\_0] Initializing block control (NOC ID: 0xDDC0000000000000)

[INFO] [0/DDC\_1] Initializing block control (NOC ID: 0xDDC0000000000000)

[INFO] [0/DUC\_0] Initializing block control (NOC ID: 0xD0C0000000000000)

[INFO] [0/DUC\_1] Initializing block control (NOC ID: 0xD0C0000000000000)

[WARNING] [CORES] Timer loopback test failed!

[WARNING] [CORES] Expecting clock rate: 200 MHz

Approximate clock rate: 325.949 MHz

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| Device: X-Series Device

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| | Mboard: X310

| | revision: 6

| | revision\_compat: 4

| | product: 30511

| | mac-addr0: 00:80:2f:21:a2:85

| | mac-addr1: 00:80:2f:21:a2:86

| | gateway: 192.168.10.1

| | ip-addr0: 192.168.10.2

| | subnet0: 255.255.255.0

| | ip-addr1: 192.168.20.2

| | subnet1: 255.255.255.0

| | ip-addr2: 192.168.30.2

| | subnet2: 255.255.255.0

| | ip-addr3: 192.168.70.2

| | subnet3: 255.255.255.0

| | serial: 30A1200

| | FW Version: 6.0

| | FPGA Version: 35.1

| | FPGA git hash: bb85bdf

| | RFNoC capable: Yes

| |

| | Time sources: internal, external, gpsdo

| | Clock sources: internal, external, gpsdo

| | Sensors: gps\_gpgga, gps\_gprmc, gps\_time, gps\_locked, gps\_servo, ref\_locked

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| | | RX Dboard: A

| | | ID: WBX v4, WBX v4 + Simple GDB (0x0063)

| | | Serial: 309B58D

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| | | | RX Frontend: 0

| | | | Name: WBXv4 RX+GDB

| | | | Antennas: TX/RX, RX2, CAL

| | | | Sensors: lo\_locked

| | | | Freq range: 25.000 to 2200.000 MHz

| | | | Gain range PGA0: 0.0 to 31.5 step 0.5 dB

| | | | Bandwidth range: 40000000.0 to 40000000.0 step 0.0 Hz

| | | | Connection Type: IQ

| | | | Uses LO offset: No

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| | | | RX Codec: A

| | | | Name: ads62p48

| | | | Gain range digital: 0.0 to 6.0 step 0.5 dB

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| | | RX Dboard: B

| | | ID: WBX v4, WBX v4 + Simple GDB (0x0063)

| | | Serial: 309B57C

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| | | | RX Frontend: 0

| | | | Name: WBXv4 RX+GDB

| | | | Antennas: TX/RX, RX2, CAL

| | | | Sensors: lo\_locked

| | | | Freq range: 25.000 to 2200.000 MHz

| | | | Gain range PGA0: 0.0 to 31.5 step 0.5 dB

| | | | Bandwidth range: 40000000.0 to 40000000.0 step 0.0 Hz

| | | | Connection Type: IQ

| | | | Uses LO offset: No

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| | | | RX Codec: B

| | | | Name: ads62p48

| | | | Gain range digital: 0.0 to 6.0 step 0.5 dB

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| | | TX Dboard: A

| | | ID: WBX v4 (0x0062)

| | | Serial: 309B58D

| | | ID: WBX + Simple GDB, WBX v3 + Simple GDB, WBX v4 + Simple GDB, WBX-120 + Simple GDB (0x004f)

| | | Serial: 3099E72

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| | | | TX Frontend: 0

| | | | Name: WBXv4 TX+GDB

| | | | Antennas: TX/RX, CAL

| | | | Sensors: lo\_locked

| | | | Freq range: 25.000 to 2200.000 MHz

| | | | Gain range PGA0: 0.0 to 31.0 step 1.0 dB

| | | | Bandwidth range: 40000000.0 to 40000000.0 step 0.0 Hz

| | | | Connection Type: IQ

| | | | Uses LO offset: No

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| | | | TX Codec: A

| | | | Name: ad9146

| | | | Gain Elements: None

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| | | TX Dboard: B

| | | ID: WBX v4 (0x0062)

| | | Serial: 309B57C

| | | ID: WBX + Simple GDB, WBX v3 + Simple GDB, WBX v4 + Simple GDB, WBX-120 + Simple GDB (0x004f)

| | | Serial: 3099E73

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| | | | TX Frontend: 0

| | | | Name: WBXv4 TX+GDB

| | | | Antennas: TX/RX, CAL

| | | | Sensors: lo\_locked

| | | | Freq range: 25.000 to 2200.000 MHz

| | | | Gain range PGA0: 0.0 to 31.0 step 1.0 dB

| | | | Bandwidth range: 40000000.0 to 40000000.0 step 0.0 Hz

| | | | Connection Type: IQ

| | | | Uses LO offset: No

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| | | | TX Codec: B

| | | | Name: ad9146

| | | | Gain Elements: None

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| | | RFNoC blocks on this device:

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| | | \* DmaFIFO\_0

| | | \* Radio\_0

| | | \* Radio\_1

| | | \* DDC\_0

| | | \* DDC\_1

| | | \* DUC\_0

| | | \* DUC\_1

klieberschnitzel@ubuntu18-04-3:

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cd /workarea-gnss-sdr/data\_RealGnssSdr

gnss-sdr --config\_file=./my\_GPS\_receiver.conf

cd /workarea-gnss-sdr/data\_SimGnssSdr

gnss-sdr --config\_file=./my-first-GNSS-SDR-receiver.conf

Timer loopback test failed